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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Application No.: 10/054,653 Confirmation No.: 9448  
First Named Inventor: Bulucea, Constantin Filing Date: 18 January 2002  
Group Art Unit: 2814 Examiner: Farahani, D.  
Atty. Docket No.: NS-5127 US  
Title: Gate-Enhanced Junction Varactor With Gradual Capacitance Variation  
Assignee(s): National Semiconductor Corporation

Mountain View, California  
28 October 2003

**MAIL STOP NON-FEE AMENDMENT  
COMMISSIONER FOR PATENTS  
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Alexandria, Virginia 22313-1450**

**PRELIMINARY AMENDMENT TO DRAWINGS**

Sir:

The drawings for the above patent application should be amended in the following manner.

In Fig. 10a, the arrow for reference symbol "156" should be shortened to end at the bottom of region 160.

In Fig. 10c, reference symbol "156" should be added with an arrow extending to the bottom of region 160.

In Fig. 10d, reference symbol "128" should be changed to "178".

In each of Figs. 14 and 15, "+" and "-" should be added respectively near the top and bottom of the double-headed arrow for reference symbol  $V_{GB}$ .

In Fig. 21, an arrowhead should be added to the remote end of the lead line for reference symbol 106.

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In Fig. 22, the number "3" should be added along the bottom of the horizontal axis at a location equidistant between the numbers "2" and "4"; the slanted shading should be completed in region 116 just above newly added number "3".

In Fig. 37, reference symbol "282" should be added to the lead line extending from the smallest dashed-line square near the center of the figure; the reference symbol for the double-headed arrow near the lower right-hand corner of the figure should be changed from "138" to "134".

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